

DESIGN OF A GRID CONNECTED SOLAR SYSTEM FOR YOLA NORTH AND SOUTH, ADAMAWA STATE

Engr. Alfred, Ba'amani Baams
Department of Electrical &
Electronics Engineering,
Modibbo Adama University of
Technology, Yola, Nigeria

Engr. Dr. Iliya Tizhe Thuku
Department of Electrical &
Electronics Engineering,
Modibbo Adama University of
Technology, Yola, Nigeria

Engr. Simon Samuel
Department of Electrical &
Electronics Engineering,
Modibbo Adama University of
Technology Yola, Nigeria.

ABSTRACT: In this research work, a grid connected solar photovoltaic (PV)-battery energy storage system (PV/BESS) was designed to improve the power supply of Yola north and south. The solar photovoltaic panel model adopted is 100W monocrystalline photovoltaic panel, and the average solar radiation of the study area is 654.40W/m². The system consists of modeled PV Array, Boost converter model, three-phase power inverter model, step-up power transformer and modeled Phase locked loop. The designed solar photovoltaic model generated 3.40MW of electric power via the step-up transformer, which was integrated into the utility grid of the study area. The output signal of the solar PV array was optimized using boost converter. The inverter inverted the output of the PV array to AC signal, which is connected to step-up power transformer to step-up the three-phase AC voltages to the grid voltages level. PLL was designed for proper synchronization of the solar PV model with the grid without harmonic distortions. The designed solar PV array model was able to supplement the deficit power of 3.40MW to the grid of the study area.

Keywords: Photovoltaic System, Boost Converter, 3-phase power Inverter, PLL, step-up power transformer and grid source.

1. INTRODUCTION

This paper focuses on the design and simulation of a grid connected PV system that will augment the shortfall in power supply in Yola North and South as well as to overcome the problem of load shedding. The increase of the study area energy demand, due to modern industrial development, population growth and global warming, encourages investments in renewable energy. Hence, alternative energy in particular solar energy serves as solution to the inadequate power supply of the study area. However, battery energy storage system (BESS) is connected to the systems for use at night and during cloudy days. The solar power system used photovoltaic (PV) cell to convert solar radiation to electricity [20]. The PV cell produces only a small amount of current and voltage. So, in order to meet a large load demand, the photovoltaic cell has to be connected into modules and the modules connected into arrays [1]. The grid connected solar

photovoltaic system is designed in cognizance of the effect of environmental weather conditions. The PV array model output signal connected to the utility network is converted to AC signals compatible with the grid AC voltage. This paper discusses the detailed modeling and simulation of the complete grid-connected PV array system. The solar PV array model is connected to the utility grid by a DC-DC boost converter that boosted the PV array model output voltage, and DC-AC inverter inverted the DC output voltage of the PV array into the three-phase AC voltages. The PI control circuit of the DC-converter was used to maintain constant DC input of the inverter. The model of the entire system is simulated in Matlab/Simulink environment. The block diagram of the design is shown in Figure 1. It consists of photovoltaic Array, DC-DC boost converter, BESS, 3-phase Inverter, 3-phase PLL, 3- phase step-up transformer, 3-phase load and 3-phase utility grid source.

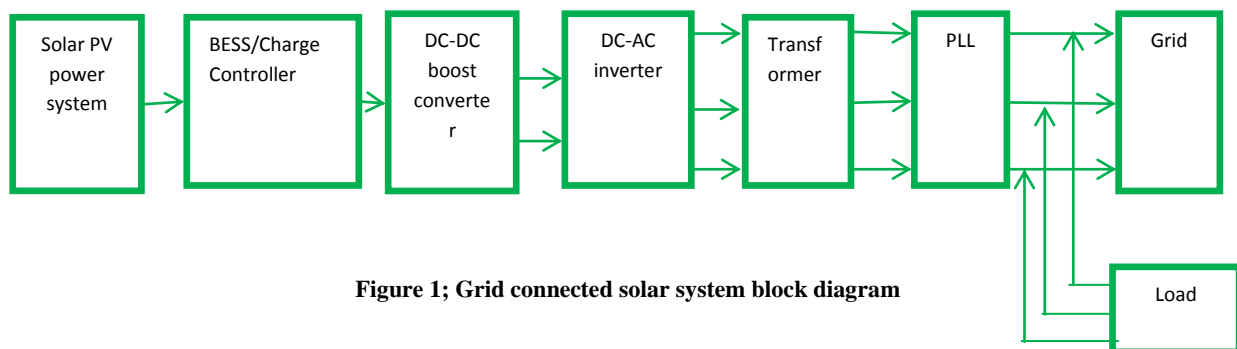


Figure 1; Grid connected solar system block diagram

2. System design

This section deals with the modeling and simulation of the complete designed of grid connected solar PV array that formed the sub-systems. The following steps were considered for the actualization of the design; Data collected on load demand and weather conditions of the study area from YEDC, TCN and NAMA Yola, design and simulation of solar PV array system in Matlab/Simulink programs and synchronization in order to control the disturbances on the lines.

3. Solar array sizing

The power of one module is not enough to meet the power requirement of the study area. Some of the modules in a PV array are connected in series to obtain the desired voltage while some are connected in parallel to produce current that made up the required power [9]. However, diodes are connected alongside the photovoltaic cells to avoid reverse currents, in case of partial or total shading [13]. The total number of photovoltaic (PV) modules required for the grid connected solar system for the study area is calculated using the equations given as follows;

$$E_r = \frac{\text{Daily average consumption demand}}{\text{product of components efficiency}}$$

$$E_r = \frac{E}{\eta_{\text{overall}}} \quad (1)$$

[6]

Where;

E_r = Require energy

E = Daily energy or demand in Watt-hours

η = Component efficiency

The peak power (P_p) is equal to the required energy (E_r) divided by the average sun hours (T_{\min}) per day for a particular geographical location [2]. The equation 2 computes the P_p .

$$P_p = \frac{\text{Daily Energy requirement}}{\text{minimum sun peak hours per day}}$$

$$P_p = \frac{E_r}{T_{\min}} \quad (2)$$

Where;

E_r = Require energy

P_p = peak power

T_{\min} = period

The total direct current (I_{dc} measure in Amp) of the photovoltaic module is given by equation 3

$$I_{dc} = \frac{\text{peak power}}{\text{system DC Voltage}} = \frac{P_p}{V_{dc}} \quad (3)$$

Where;

I_{dc} = total dc current

P_p = Peak power

V_{dc} = DC-voltage

The mathematical expression of modules connected in series (N_s) and parallel (N_p) for the production of the current and

voltage obtain the power demand of the study area are presented in equations 4 and 5.

$$N_s = \frac{\text{system DC Voltage}}{\text{One module Rated Voltage}} = \frac{V_{dc}}{V_r} \quad (4)$$

$$N_p = \frac{\text{Whole Module current}}{\text{Rated current of one module}} = \frac{I_{dc}}{I_r} \quad (5)$$

[16]

Where;

N_s = Number of modules in series

N_p = Number of modules in parallel

V_r = Rated voltage of each module

I_r = Rated current of a single module

I_{dc} = Whole DC current

The total number of photovoltaic modules needed for the power requirement of the study area is calculated using equation 6.

$$N_m = N_s \times N_p \quad (6)$$

P_p of 3.40MW and V_{dc} of the system 4000V

The total dc current (I_{dc}) is calculated using the equation 3 to give;

$$I_{dc} = \frac{\text{peak power}}{\text{System DC Voltage}} = \frac{P_p}{V_{dc}}$$

$$I_{dc} = \frac{3.40\text{MW}}{4000\text{V}} = 850\text{A}$$

To determine the number of PV modules in series (N_s) the equation 4 is used;

$$N_s = \frac{\text{system DC Voltage}}{\text{One module Rated Voltage}}$$

$$N_s = \frac{V_{dc}}{V_r} = \frac{4000\text{V}}{17.40\text{V}} = 230 \text{ modules.}$$

Calculation of number of modules in parallel using equation 5 is shown as Follow;

$$N_p = \frac{\text{Whole Module current}}{\text{Rated current of one module}}$$

$$N_p = \frac{I_{dc}}{I_r} = \frac{850\text{A}}{5.75\text{A}} = 148 \text{ modules.}$$

The total number of modules required for the complete design of the system is calculated using equation 6 as follows:-

$$N_m = N_s \times N_p = 230 * 148 = 34,040 \text{ Modules}$$

4. PV System Modeling

Photovoltaic module has standard mathematical modeling equations [12]. These equations are used in modeling and

simulation of the system using matlab-simulink program. The characteristic equations of solar photovoltaic cell are applied in the formation of the photovoltaic arrays. These characteristic equations of the PV cell are given as;

i. Reverse saturation current (I_{rr}) equation;

$$I_{rr} = \frac{I_{scr}}{\exp\left(\frac{QV_{oc}}{N_s k A T_{rk}} - 1\right)} \quad (7)$$

Where;

I_{scr} = Short Circuit Current = 6.32A,
 Q = Electron charge = 1.6×10^{-19} C,
 V_{oc} = Open circuit v
 Voltage = 21.60V
 N_s = Number of modules in series = 230
 k = Boltzmann's constant = 1.3805×10^{-23} J/K,
 A = Ideality factor of diode = 1.3,
 T_{rk} = Reference temperature = 298.15K

ii. Band gap of solar cell (E_G) equation;

$$E_G = E_{G(0)} - \frac{\alpha(T_{op})^2}{T_{op} + \beta} \quad (8)$$

[17]

Where;

α = alpha = 0.473
 $E_G(0)$ = 1.166
 β = beta = 636
 T_{op} = operating temperature = 305.7K

iii. Reversed saturation current at T_{op} (I_{rs});

$$I_{rs} = I_{rr} \left[\frac{T_{op}}{T_{rk}} \right] * \text{Exp} \left(\frac{Q E_G}{K A} \left[\frac{1}{T_{op}} - \frac{1}{T_{rk}} \right] \right) \quad (9)$$

Where;

I_{rr} = Reverse saturation current
 T_{op} = operating temperature = 305.7K
 T_{rk} = Reference temperature = 298.15K
 Q = Electron charge = 1.6×10^{-19} C
 E_G = Band gap energy
 k = Boltzmann's constant = 1.3805×10^{-23} J/K
 A = Ideality factor of diode = 1.3

iv. Photo-current (I_{ph}) equation;

$$I_{ph} = [I_{scr} + K_i(T_{op} - T_{rk})] * S \quad (10)$$

Where;

I_{scr} = Short circuit current = 6.32A.
 K_i = short-circuit current of cell at = 0.00023
 T_{op} = operating temperature = 305.7K
 T_{rk} = Reference temperature = 298.15K
 S = solar irradiation (W/m²) of the study area with average value 654.40 W/m²

v. Thermal Voltage (V_{th}) equation;

$$V_{th} = \frac{Q}{k T_{op}} \quad (11)$$

[4]

Where;

Q = Electron charge = 1.6×10^{-19} C

k = Boltzmann's constant = 1.3805×10^{-23} J/K
 T_{op} = operating temperature = 305.7K

vi. Current source output (I) equation;

$$I = N_p I_{ph} - N_p I_{rs} \left[\exp\left(\frac{Q}{A k T} * \frac{V_{th}}{N_s}\right) - 1 \right] \quad (12)$$

Where;

N_p = Number of modules in parallel = 148
 I_{ph} = Photo-current
 I_{rs} = Reversed saturation current at T_{op}
 Q = Electron charge = 1.6×10^{-19} C
 k = Boltzmann's constant = 1.3805×10^{-23} J/K
 A = Ideality factor of diode = 1.3
 V_{th} = Thermal Voltage
 N_s = No. of modules in series = 230
 [14]

The mathematical equations of these components were modeled individually and networked together to determine the functionality of the PV array. Hence, these equations are important for the design of the PV array [5], because it determine the desired output of the system. The first step in photovoltaic system modeling is to know the PV array size, determined using equation 6.

5. Modeled solar PV array system.

The modeling and simulation of the components of the photovoltaic system formed the masked subsystem of the PV array as shown in Figure 2. The subsystem of the PV array was simulated on a matlab-simulink environment. The result of the simulated solar PV array masked subsystem was satisfactory, and it serves as an input to the boost converter.

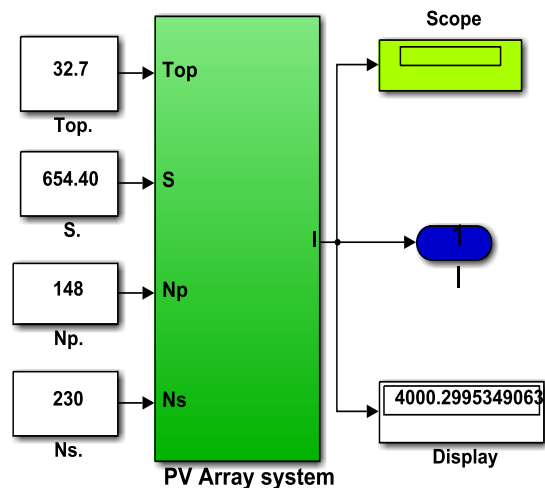
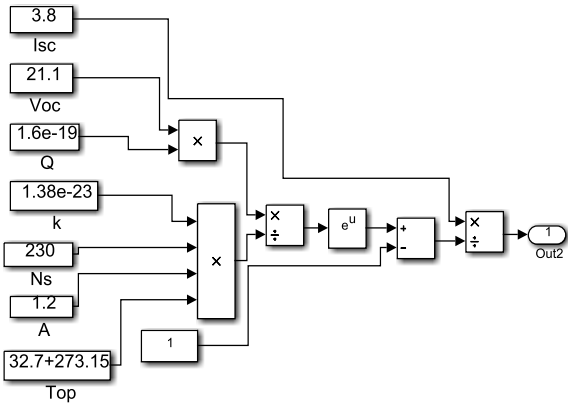


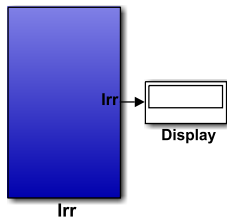
Figure 2; Modeled Photovoltaic (PV) array system

The internal structure of the masked subsystem of PV array of figure 2 such as figure 3, 4, 5, 6, 7 and 8 are given as follows;

5.1; Reverse saturation current (I_{rr}) model



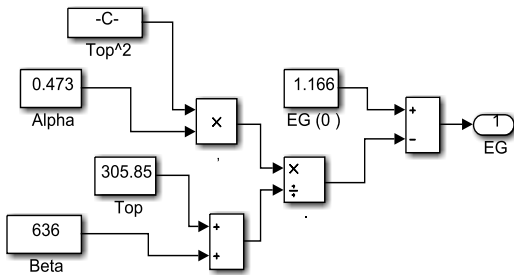
(a) Irr simulink blocks.



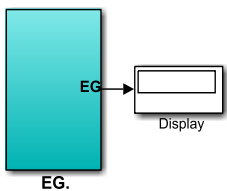
(b) Irr subsystem

Figure 3; (a) Reversed saturation current (I_{rr}) Simulink Block and (b) I_{rr} simulink block converted to I_{rr} subsystem

5.2. Band gap of the solar cell (EG) model



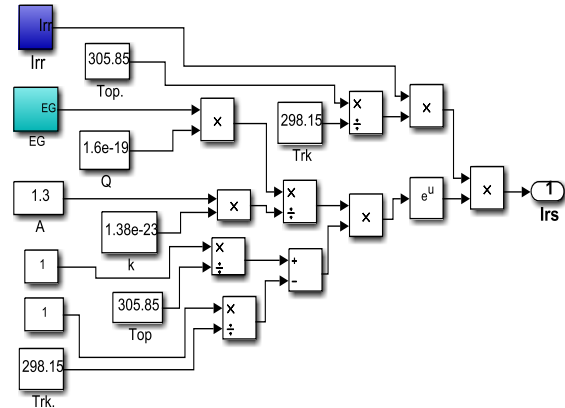
(a) EG simulink blocks.



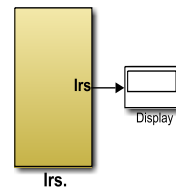
(b) EG subsystem

Figure 4; (a) Band gap of the solar cell (EG) Simulink block and (b) EG Simulink block converted to EG subsystem

5.3; Reversed saturation current at T_{op} (I_{rs}) model



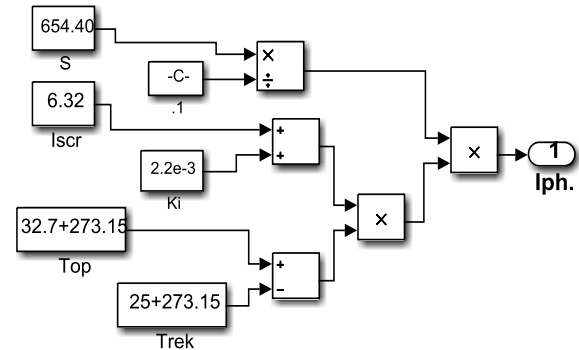
(a) Irs simulink blocks.



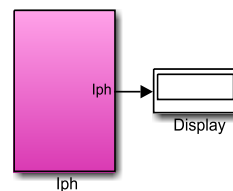
(b) Irs subsystem

Figure 5; (a) Reversed saturation current at T_{OP} (I_{rs}) simulink block and (b) I_{rs} simulink block converted to subsystem

5.4; Photo-current (I_{ph}) model



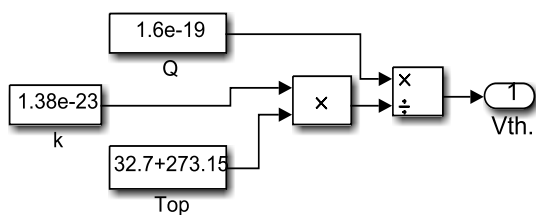
(a) Iph simulink blocks.



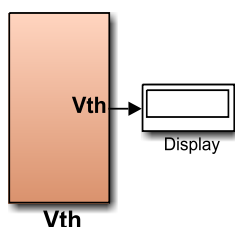
(b) Iph subsystem

Figure 6; (a) Photovoltaic Current (I_{ph}) Simulink Block and (b) I_{ph} Simulink Block converted to subsystem

5.5; Thermal voltage (V) model



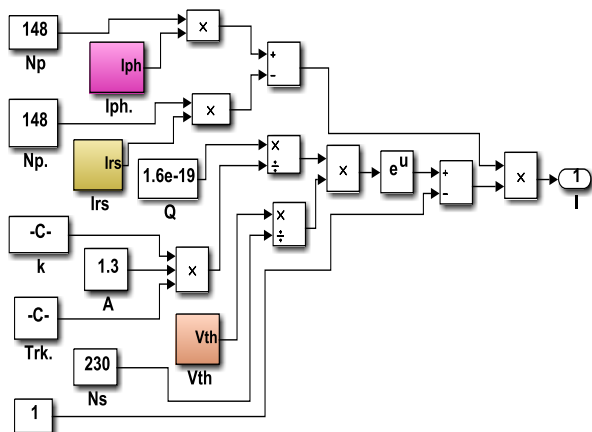
(a) Vth simulink blocks.



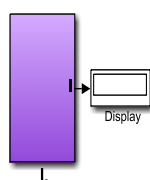
(b) Vth subsystem

Figure 7; (a) Thermal Voltage (V) Simulink Block and (b) V Simulink Block converted to subsystem.

5.6; Current source output (I) model



(a) I simulink blocks.



(b) I subsystem

Figure 8; (a) Current source output (I) simulink block and (b) I simulink block converted to subsystem.

7; Design and modeling of the Boost Converter

The boost converter is a system designed to convert low input signal (DC voltage) to high output signal (DC voltage). The unregulated input DC signal is filtered and then boosted to regulated output DC signal [7]. Here, the input DC source of the converter is PV output signal (DC voltage), which is boosted to a desired output of the converter. The design of boost converter is important, because it will help in controlling both the current (CRF) and voltage ripples (VEF). CRF was selected as 10% and VRF was also selected as 5%, in line with International Electro-technical Commission (IEC) harmonized standard which is bounded within the range 0-30% [12]. Where Switching Frequency (f_s)=100KHz, $\Delta I_o/I_o = 10\%$ and $\Delta V_o/V_o = 5\%$ are values for CRF and VRF used in the design to produce output with minimal ripples. The capacitor, inductor and the duty cycle are calculated using the given parameters of Input voltage (V_g) and output voltage (V_o) of the system converter where input and output voltage is 4000V and 7400V.

$$V_g = 4000V,$$

$$V_o = 7400V$$

$$I_o = 275A$$

Calculation of the duty cycle (D):-

$$V_o/V_g = 1/((1 - D))$$

$$1/((1 - D)) = V_o/V_g$$

$$1/((1 - D)) = 7262/4000 = 0.449$$

$$D = 0.449 \quad (\text{duty cycle } D)$$

Inductor (L) calculation:-

$$\Delta I = (0.1 * 275) A = 27.5A$$

$$L = \frac{DV_g}{f_s(\Delta I_L)} = \frac{(4000 * 0.449)}{(275 * 100 * 10^3)} = \frac{1796}{27500000}$$

$$L = 0.0000653H = 0.065mH$$

$$R_o = V_o/I_o = 7262/275 = 26.41 = 26.41\Omega$$

Calculation of the Capacitor (C) as follows:-

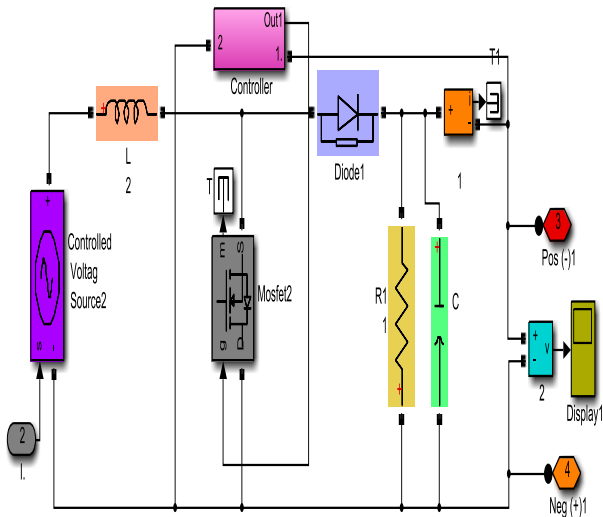
$$\frac{\Delta V_o}{V_o} = \frac{DT_s}{CR_o} \quad \text{where } C = \frac{D}{R_o f} * \frac{1}{\frac{\Delta V_o}{V_o}}$$

Therefore,

$$C = \frac{0.449}{26.41 * 100 * 10^3 * 0.05}$$

$$C = 3.4\mu F$$

The boost converter of figure 9 was modeled and simulated on Matlab-Simulink environment, which then boosted the input DC voltage to 7400V. However, the pulse width modulation controller and its duty cycle were used to turn and stabilize the converter output. Hence, figure 10 shows the PV array connected to boost converter model



(b) DC-DC boost converter Simulink block

Figure 9; Boost converter simulink block.

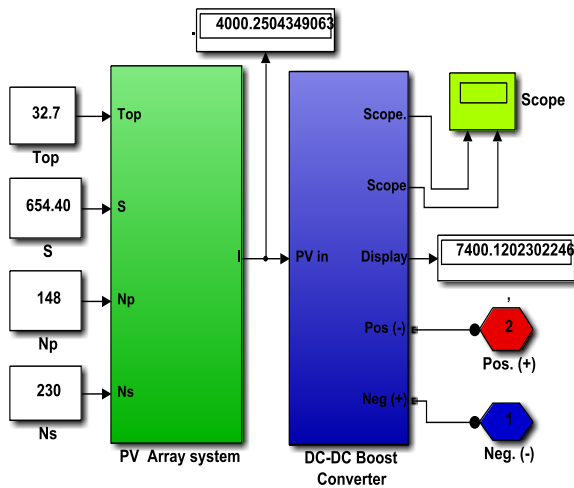


Figure 10; The interfaced of the PV array and boost converter model

8; Design of Battery energy storage system (BESS)

The battery storage system is the most efficient method of storing generated energy from solar PV system for use at night, cloudy, rainy and dusty days [18]. The BESS also maintaining power stability between the PV system and the utility grid [15]. In sizing the battery bank estimated energy storage is first determined [3]. The type of battery used is deep-cycle lead acid. The capacity of the entire battery bank is computed in ampere-hours, Electrical characteristics of the battery are given as; Number of Days of Autonomy (D_{aut}) = 3
Where

E_{est} = Estimated Energy Storage

E_{safe} (Wh) = Safe Energy Storage

C_{tb} (Ah) = Total Capacity of Battery Bank

N_{tb} = Total Number of Batteries in Bank

N_{sb} = Number of Batteries in Series

N_{pb} = Number of Batteries in Parallel

C_b = 250 Ah,

V_b = 12 V,

D_{disch} = 80%

$$E_{est} = E_d * D_{aut} \quad (13)$$

$$E_{safe} = \frac{E_{est}}{D_{disch}} \quad (14)$$

$$C_{tb} = \frac{E_{safe}}{V_b} \quad (15)$$

$$N_{tb} = \frac{C_{tb}}{C_b} \quad (16)$$

$$N_{sb} = \frac{V_{dc}}{V_b} \quad (17)$$

$$N_{pb} = \frac{N_b}{N_{sb}} \quad (18)$$

[10]

$$E_{est} = E_d * D_{aut} = 3400000 * 3 = 10200Kwh$$

$$E_{safe} = \frac{E_{est}}{D_{disch}} = \frac{10200000}{80\%} = 12750Kwh$$

$$C_{tb} = \frac{E_{safe}}{V_b} = \frac{12750000}{12} = 1062.5KAh$$

$$N_{tb} = \frac{C_{tb}}{C_b} = \frac{1062500}{250} = 4250 Batteries$$

(Total Batteries required = 4250)

$$N_{sb} = \frac{V_{dc}}{V_b} = \frac{4000}{12} = 333.3$$

(Number of Batteries in Series = 333.3)

$$N_{pb} = \frac{N_b}{N_{sb}} = \frac{4250}{333.3} = 12.8$$

(Number of Batteries in parallel = 12.8)

9; Charge controller (CC) design

A battery charge controller (BCC) is a device that controls the power flow within the system and regulates the system voltage to a specified range. Its purpose is to keep batteries properly fed and safe for the long term [11]. The system also block reverse current and control battery overcharge [18]. The design of a suitable CC starts by computing the required total current that the controller should withstand. The properties of the selected CC are as follows:

V_{cc} = 24,

I_{cc} = 60 A (dc),

Safety Factor (F_{safe}) = 1.25

The numbers of desired CC are calculated as follows;

N_{ccn} = Number of Charge Controllers

I_{rcc} (A) = Required CC Current

N_{pm} = Number of modules in parallel = 148

$I_{sc}M$ = Short circuit current of the selected module = 6.32A

N_{cc} = Number of CC

$$I_{rcc} = I_{sc}M * N_{pm} * F_{safe} \quad (19)$$

$$N_{cc} = \frac{I_{rcc}}{I_{cc}} \quad (20)$$

$$I_{rcc} = I_{sc}M * N_{pm} * F_{safe} = 6.32 * 148 * 1.25$$

$$I_{rcc} = 1169.2A$$

$$N_{cc} = \frac{I_{rcc}}{I_{cc}} = \frac{1169.2}{60} = 19.49 = 20$$

Number of Charge Controllers required = 20

10; Power AC Inverter design

All power generating plants that generate DC power normally need inverters to convert the power generated to AC power for use by AC rated appliances [11]. The subsystem of PV system-inverter shown in figure 11 is a complete block diagram of interfaced PV array model, DC converter and AC inverter model. The 3-phase AC output signals of the PV system-inverter model are the signals injected into the grid. The output of this model is a 3-phase sinusoidal signals module serves as an input to 3-phase step-up transformer to be connected to the grid system.

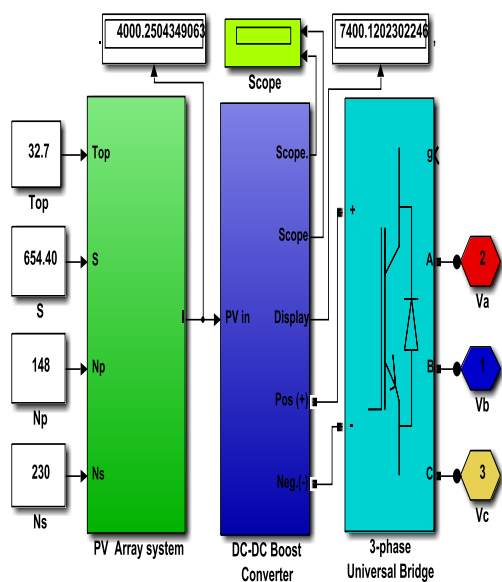


Figure 11; The block diagram of complete interfaced PV system with Booster and inverter.

11; Phase locked loop (PLL) modeling

The PLL is designed to control the phase difference and other harmonic distortion. The phase of generated signals of solar system is automatically adjusted to match the phase of an input signals of the grid. Figure 12 is the PLL Simulink block and as well an internal structure of figure 13. The model was simulated using Matlab/Simulink software.

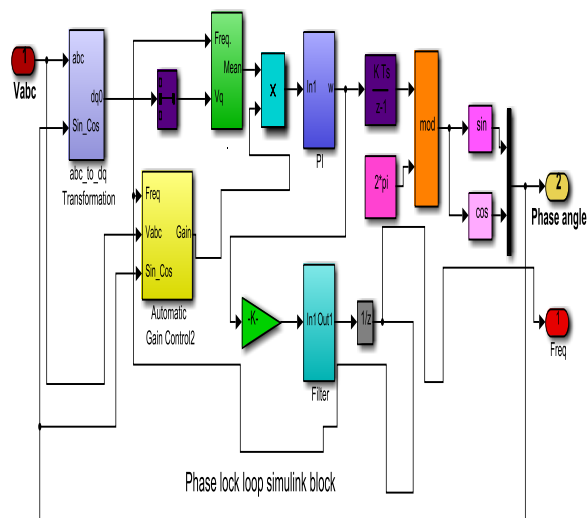


Figure 12; PLL Simulink block

The three blocks subsystem of PLL shown in figure 13 consists of the followings; phase detector (PD), loop filter (LF) and voltage controlled oscillator VCO. The PD is a comparator, which compares the input signals and the feedback signals. The output signal generated by the PD is proportional to the phase difference between the input signals and the signals generated by the internal oscillator called VCO. The output of the PD is an input to the LF. The LF is characterized with low pass filtering to attenuate high-frequency AC components from the PD output and provide DC controlled signal to VCO which acts as an integrator. The VCO generates at its output a periodic AC voltage signal provided by the LF [19].

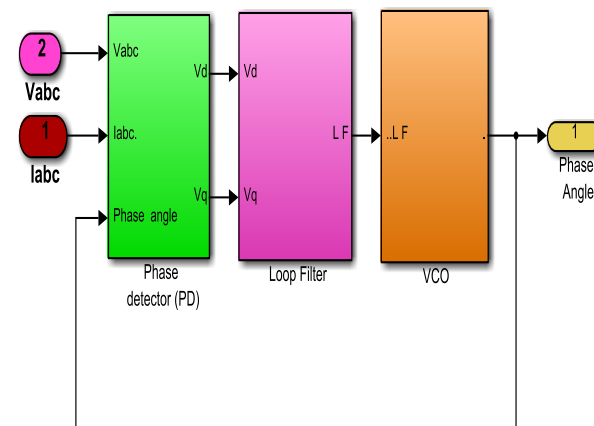


Figure 13; Subsystems of the PLL

The complete sub-system of the PLL is shown in Figure 14 which formed by masking the three blocks subsystem of PLL of figure 13.

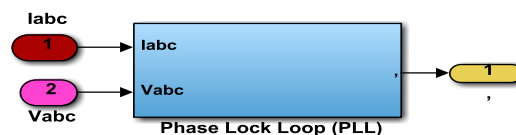


Figure 14; Subsystem of the phase locked loop (PLL)

12; The complete modeled grid-connected solar system

The model is a network of the major subsystems which comprises of the following; PV array model that saves as a DC voltage source, DC- DC boost converter model, power AC inverter model, PLL model, step up transformer, protective and measuring devices, grid source (AC), and three-phase Load. The modeling and simulation were done on individual subsystems. The PV array model was simulated, which generated 4000V DC voltage as the main DC source to the boost converter. The generated 4000V was then optimized by the converter to 7400V DC voltage. The 7400V output DC voltage of the converter model is converted to 3-phase AC voltages (V_{abc}) by the power inverter. However, the 3-phase output AC voltage (V_{abc}) of the inverter is connected to 3-phase step up transformer, which synchronized with the utility grid voltage of 33KV.

13; The modeled complete photovoltaic grid connected without phase locked loop (PLL).

The model of figure 15 was simulated without the PLL. The result obtained from the simulated grid connected solar PV

array model without PLL was not quite clear as displayed by the waveforms. Based on observation, the system output is distorted by flickers. Therefore, it clearly shows that, both the solar PV array system and the utility grid are not having same characteristics.

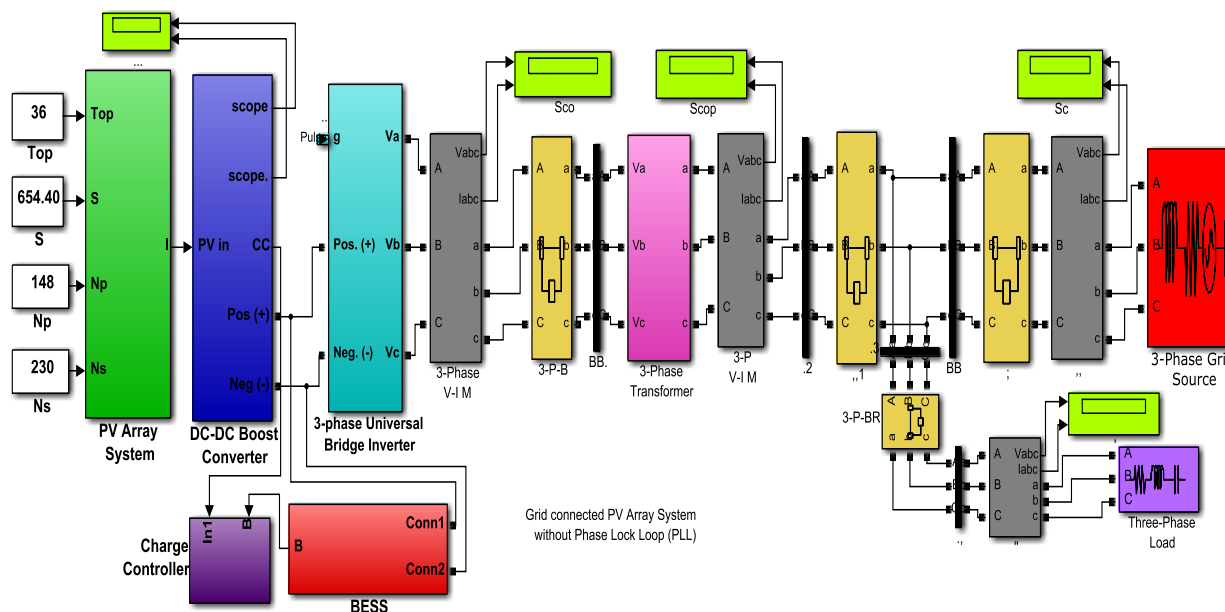


Figure 15; Simulink model of complete PV grid connected without PLL system

14; The modeled grid connected PV system with PLL

The grid-connected PV array model with PLL is shown in figure 16. Here, PLL was connected to figure 16 Model because of the distorted output of figure 15 model. The device was used to clear flickers on the lines such as phase difference, harmonics. So, synchronizing the solar PV array

system with the utility grid using PLL has given a better result. The results obtained from the simulated figure 15 model shows that the model output AC voltage is 33KV with the same frequency, amplitude and phase angle. The result obtained indicated that the grid-connected solar PV system with battery energy storage system have succeeded in tackling the problem of power supply in the study area.

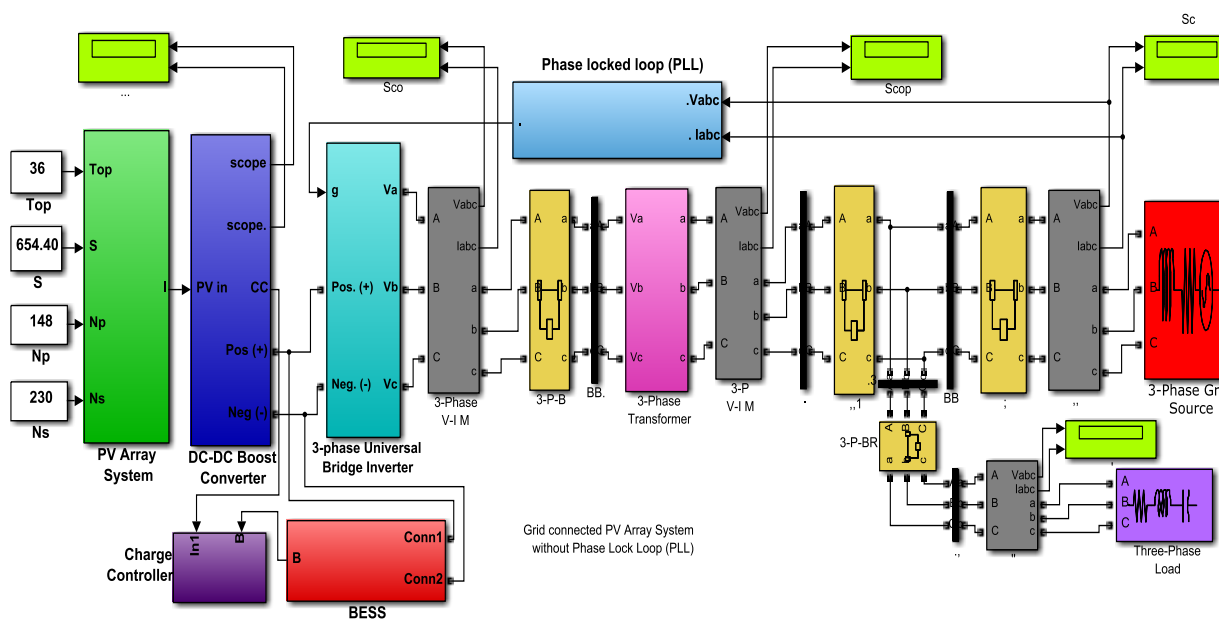


Figure 16; Simulink model of complete photovoltaic grid connected with PLL.

15; Modeled photovoltaic array system result.

The simulated result of PV array model of figure 2 is shown in figure 17. The output of the simulated PV array model consists of DC voltage (V_{dc}), DC current (I_{dc}) and

power (P) waveforms displayed by the scope, as shown in figure 16. Hence, the generated DC voltage of the PV array system is 4000V as displayed by the digital display system. Therefore, based on the output of the photovoltaic array the desired result has been obtained.

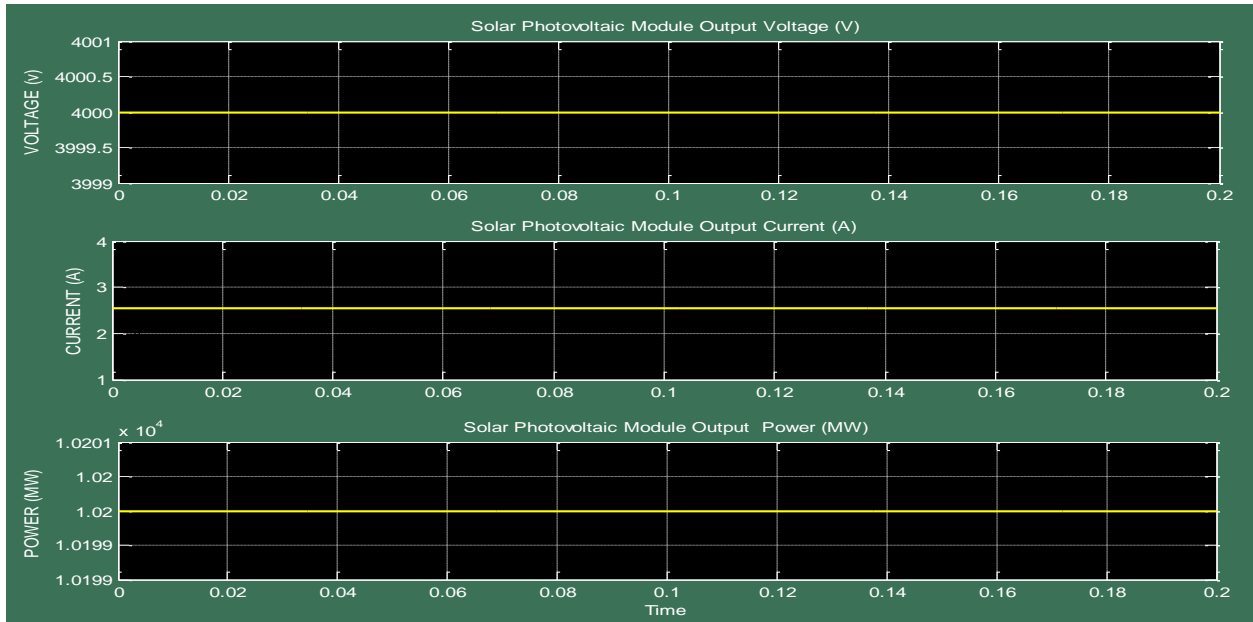


Figure 17; Solar PV array output result consists of DC-voltage, current and power.

16; The result of DC-DC boost converter.

The PV-boost converter model of figure 10 was simulated in Matlab/Simulink environment according to the standards IEC with result shown in figure 18 and 19. The scope and digital display system was connected at the output terminal

of the converter for the output result analysis. The generated 4000V DC source of the PV array model was boosted to 7400V_{dc}.

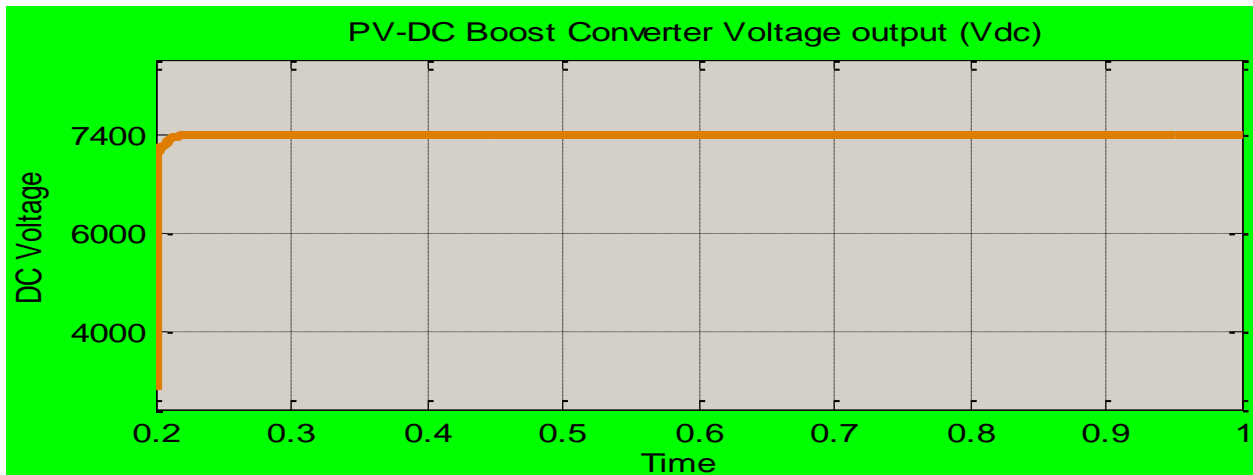


Figure 18: Photovoltaic-DC Boost Converter voltage output (Vdc)

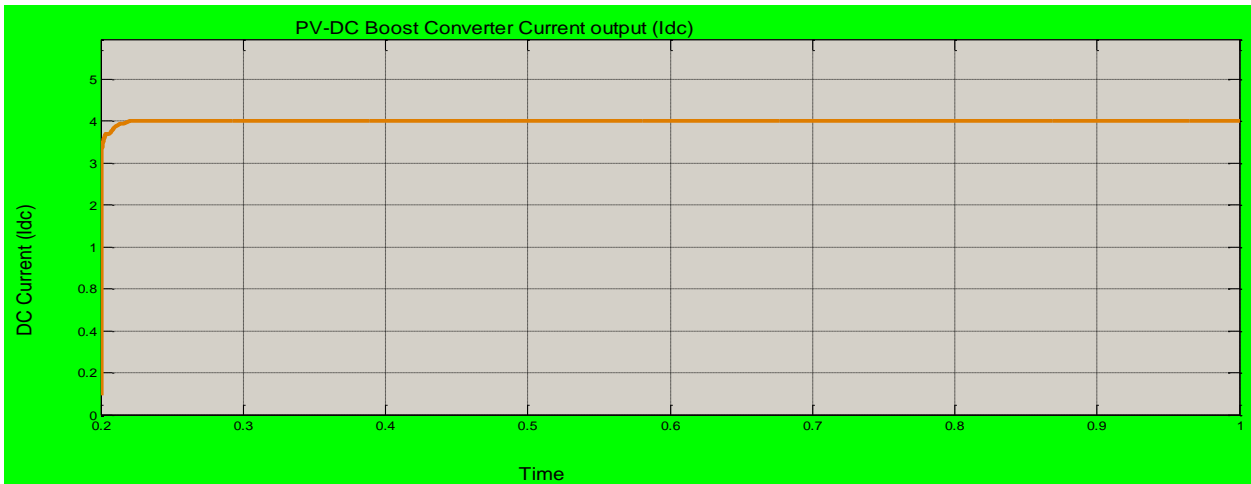


Figure 19: Photovoltaic-DC Boost Converter current output (Idc)

17; Three-phase universal bridge inverter output.

Matlab/Simulink software was used in the simulation of complete system of figure 11, given the output result of generated three-phase AC waveforms of figure 20. The

inverter does not boost or step-up the input voltage but generate the input signals into sinusoidal AC signals. In this case V_{abc} is fed to a step-up transformer for the purpose of boosting the AC signal to a grid voltage level of 33KV.

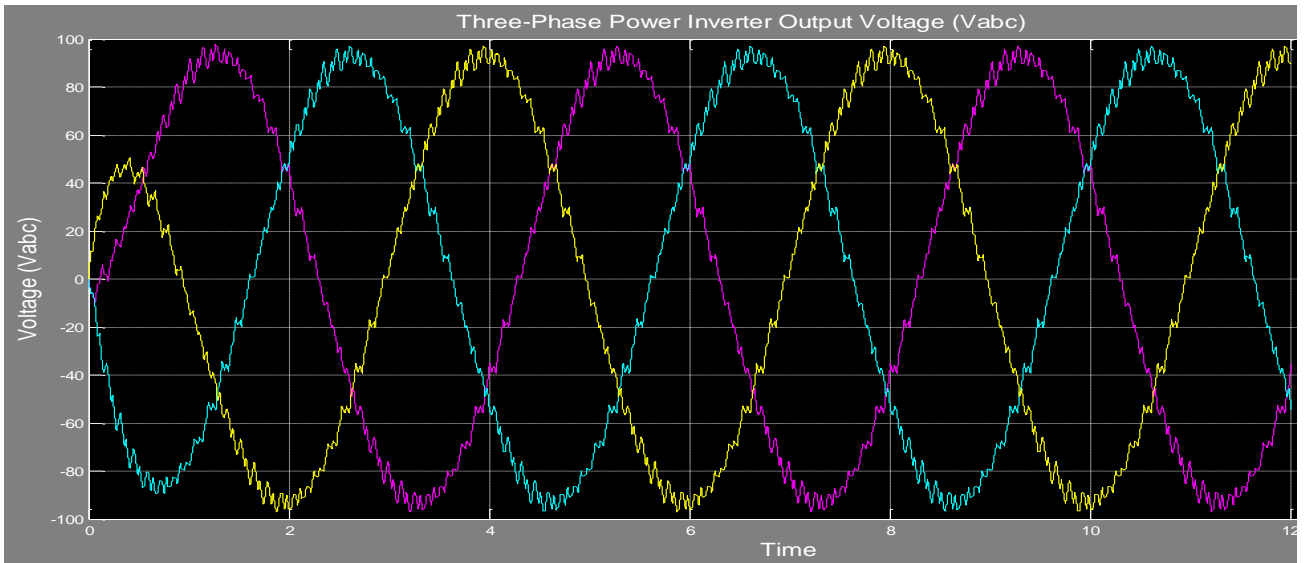


Figure 20; Three-phase universal bridge inverter result.

18; Result of complete solar PV grid system connected without PLL system;

The complete grid connected solar PV model of figure 15 was connected without control (PLL). The simulation of the modeled grid connected solar PV system of figure 15 output result is shown in figure 21. The output result

clearly indicated that the synchronization was unbalanced due to the absent of PLL. Therefore, due to the poor result obtained from the system PLL is needed in the system to clear the distortions on the lines

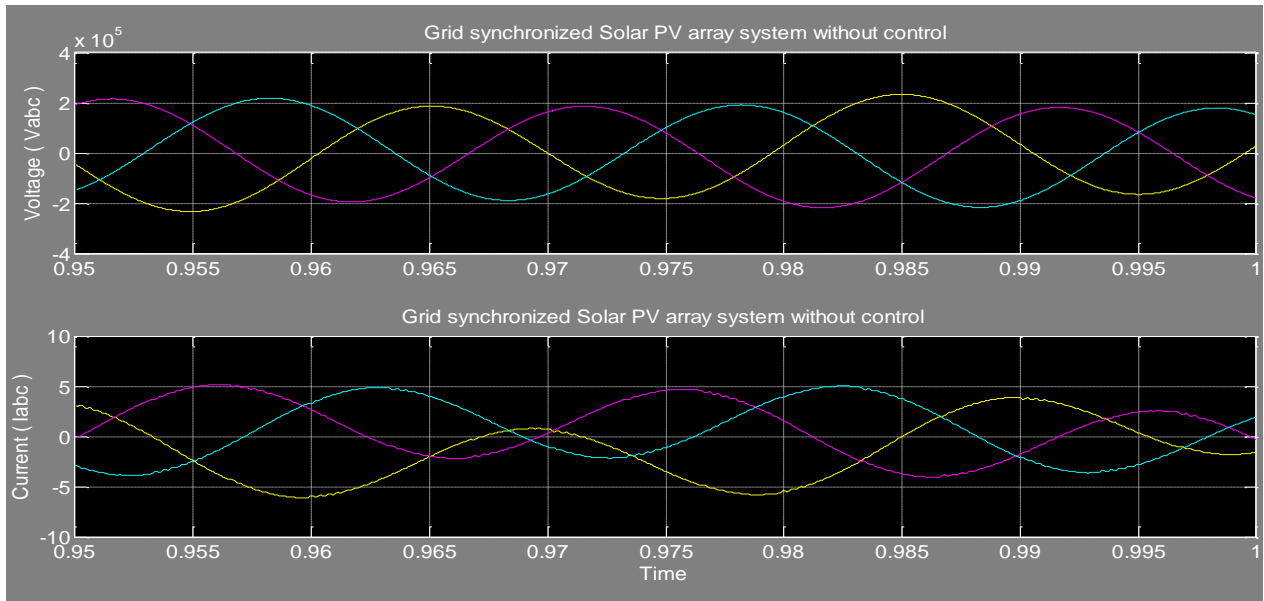


Figure 21; The result of grid synchronized Solar PV array system without a PLL system

19; Result of simulated complete grid connected solar PV system with PLL

The simulated result of the grid-connected solar PV system of figure 16 synchronized with PLL is shown in figure 22. The introduction of the PLL into the system was due to the unbalanced output result observed from figure 20. The selection of Phase lock loop was due to its high degree of

immunity and insensitivity to disturbances, harmonics, unbalance and sags. Hence, based on the analyzed result, the generated 3.40KV by the PV array system injected into the utility grid network, via the three-phase step-up transformer has been satisfactory, and has complied with IEC standards.

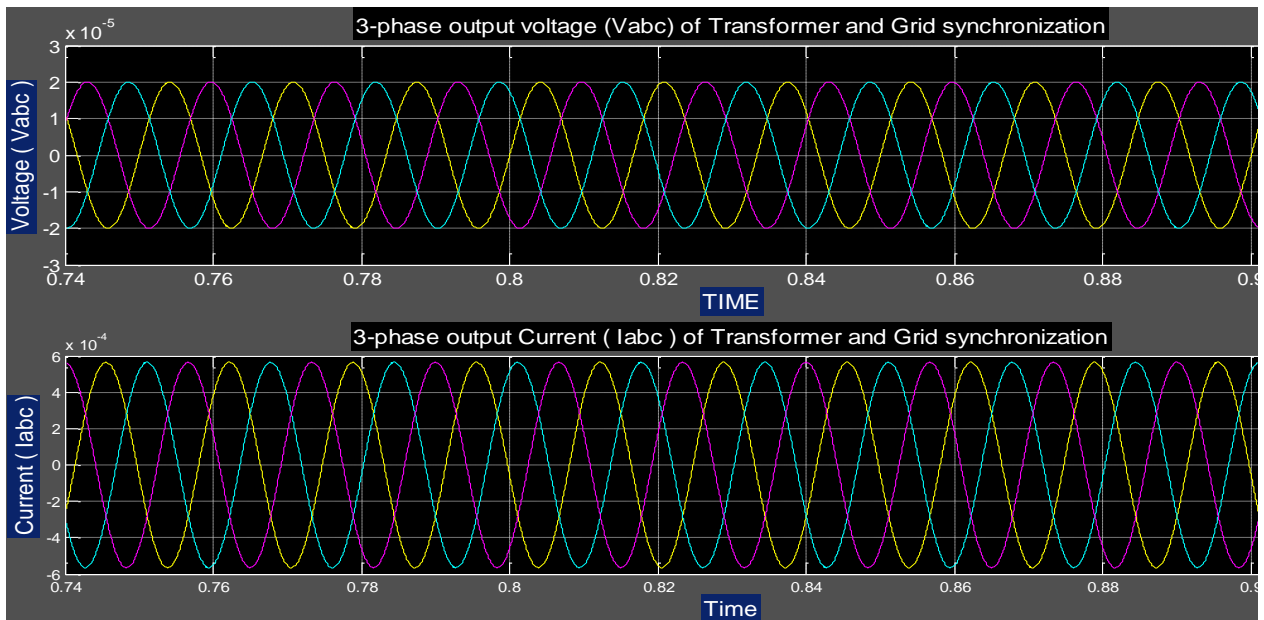


Figure 22; Solar PV array and Grid synchronization output.

20; Discussion of Results

Solar energy sources are important part of energy supply to the utility grid Network. In this study, grid connected photovoltaic-battery energy storage system generation system was modeled and simulated in MATLAB/Simulink environment. The PV array was designed based on the standard mathematical modeling equations, and as well considering the effect of environmental weather conditions (temperature, relative humidity, rainfall, solar radiation and sunshine). The equations are put together in the form of block diagrams, and simulation is carried-out using matlab-simulink program. The photovoltaic array subsystem model was simulated using Simulink program, which generated 4000V DC voltage as the main DC source. The designed photovoltaic (PV) array system was able to supplement power shortfall of 3.40MW of the study area. The DC/DC boost converter was connected to photovoltaic (PV) array where the output DC voltage of the photovoltaic (PV) system was boosted by the designed boost converter to the desired output DC voltage of 7400V. The battery energy storage system (BESS) formed part of the system, which charged or discharge by using the DC/DC bidirectional converter to maintain the power balance between photovoltaic (PV) power generation and the load demand. Simulation results prove that the grid-connected photovoltaic-battery energy storage system (PV/BESS) provides stable and reliable power for loads or power grid during its operation modes. However, the output DC voltage of the converter was inverted to 3-phase AC voltages (Vabc) using inverter, which was synchronized with 150MVA step up transformer used to step it up to the utility grid voltage of 33KV. The transformer's output (i.e. 33KV), is the output of the entire system that is synchronized with the utility grid. The synchronizer (PLL) was connected to the system to clear disturbances on the lines. The results obtained from the simulated system are the same frequency, amplitude, and phase angle, which indicated that the designed grid-connected solar photovoltaic system have succeeded in tackling the problem of power supply in the study area.

21; Conclusion

This thesis presents a complete modeled grid-connected solar system that includes solar photovoltaic array system, DC-DC converter, battery energy storage system, phase locked loop, power inverter, step-up transformer and grid source. The objectives for this thesis have been successfully realized through the modeling, simulation and performance analysis of designed system using Matlab/Simulink tools. The designed system was based on weather condition and load demand obtained from the YEDC that gives the power supply deficit 3.40MW of the study area. The shortfall (3.40MW) of power supply from the grid was augmented by the designed solar photovoltaic-battery energy storage system (PV/BESS). Based on the result the solar photovoltaic array system was able to inject a total power of 3.40MW to the grid.

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